

**PATENT**

**DOCKET NO.: Intel 2207/618602**  
**ASSIGNEE: Intel Corporation**

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

APPLICANTS : Per HAMMARLUND et al.

CONTINUATION  
OF SERIAL NO. : 09/224,377

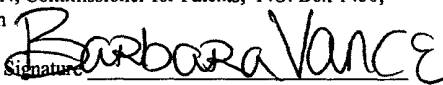
PARENT APPLICATION  
FILED : December 31, 1998

FOR : ACCESS CONTROL OF A RESOURCE  
SHARED BETWEEN COMPONENTS

GROUP ART UNIT : 2171

EXAMINER : Te Y. CHEN

M/S: PATENT APPLICATION  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Certificate of Mailing	
I hereby certify that this correspondence is being deposited with the United States Postal Service as Express Mail No. EV351181217US in an envelope addressed to: M/S: PATENT APPLICATION, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on	
Date: September 10, 2003	Signature: 
	Barbara Vance

**INFORMATION DISCLOSURE STATEMENT**

Dear Sir:

Submitted with the Continuation application, is an Information Disclosure Statement under 37 CFR §1.97(b)(3) with eleven (11) reference(s) listed on the attached PTO-1449 (modified) form. Unless otherwise indicated herein, one copy of each reference is attached. It is respectfully requested that the information be expressly considered during the prosecution of this application, and that the reference(s) be made of record therein and appear among the "References Cited" on any patent to issue therefrom.

If any fee is required, the Office is hereby authorized to charge any fees, or credit any overpayments under 37 CFR §1.17(p) to Kenyon & Kenyon, Deposit Account No. **11-0600**.

Respectfully submitted,

KENYON & KENYON

Dated: September 10, 2003

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<b>Form PTO-1449 (modified)</b>  <b>LIST OF PATENTS AND PUBLICATIONS FOR APPLICANT(S)' INFORMATION DISCLOSURE STATEMENT</b>	<b>Attorney Docket No.</b> Intel 2207/618602	<b>Parent Serial No.</b> 09/224,377
	<b>Applicant</b> Per HAMMARLUND et al.	
	<b>Parent Filing Date</b> December 31, 1998	<b>Group Art Unit</b> 2171

**U. S. PATENT DOCUMENTS**

EXAMINER INITIAL	PATENT NUMBER	PATENT DATE	NAME	CLASS	SUBCLASS	FILING DATE
	5,829,051	October 27, 1998	Steely, Jr. et al.	711	216	April 4, 1994
	5,832,534	November 3, 1998	Singh et al.	711	141	October 31, 1995
	5,845,331	December 1, 1998	Carter et al.	711	163	September 28, 1994
	5,903,908	May 11, 1999	Singh et al.	711	122	October 15, 1996
	6,105,111	August 15, 2000	Hammarlund et al.	711	136	March 31, 1998

**FOREIGN PATENT DOCUMENTS**

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
						YES	NO

**OTHER DOCUMENTS**

EXAMINER INITIAL	AUTHOR, TITLE, DATE, PERTINENT PAGES, ETC.
	Peter Song, "Multithreading Comes of Age - Multithread Processors Can Boost Throughput on Servers, Media Processors", dated July 14, 1997, pp. 13-18
	Dean M. Tullsen, Susan J. Eggers, Joel S. Emer, Henry M. Levy, Jack L. Lo, and Rebecca L. Stamm, Proceedings from the 23rd Annual International Symposium on Computer Architecture, "Exploiting Choice: Instruction Fetch and Issue on an Implementable Simultaneous Multithreading Processor", dated May 22-24, 1996, pp. 191-202
	Richard J. Eickemeyer, Ross E. Johnson, Steven R. Kunkel, Mark S. Squillante and Shiafun Liu, Proceedings from the 23rd Annual International Symposium on Computer Architecture, "Evaluation of Multithread Uniprocessors for Commercial Application Environments", dated May 22-24, 1996, pp. 203-212
	Dennis Lee, Jean-Loup Baer, Brad Calder and Dick Grunwald, "Instruction Cache Fetch Policies for Speculative Execution", pp. 1-11
	James Laudon, Anoop Gupta, and Mark Horowitz, Multithreaded Computer Architecture: A Summary of the State of the Art, "Architectural and Implementation Tradeoffs in the Design of Multiple-Context Processors", pp. 166-200
	Simon W. Moore, University of Cambridge, England, "Multithreaded Processor Design", copyright 1996, pp. 1-141

EXAMINER	DATE CONSIDERED
EXAMINER: Initial if citation considered, whether or not citation is in conformance with M.P.E.P. 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.	